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## University Examinations 2011／2012

FIRST YEAR，SECOND SEMESTER EXAMINATIONS FOR THE DEGREE OF BACHELOR OF COMPUTER SCIENCE AND FIRST YEAR，SECOND YEAR FOR THE DEGREE OF BACHELOR OF SCIENCE IN INFORMATION TECHNOLOGY．

ICS 2200：ELECTRONICS
a．What is a zener diode？
b．Differentiate between：
i．Donor and Acceptor Impurities
ii．Valence and Conduction Band
c．State the f actors that should be put into consideration when biasing a transistor
（3 Marks）
d．Show that the gain of an inverting amplifier is given by $\mathrm{Av}=\frac{R_{f}}{R_{1}}$ where $\mathrm{R}_{\mathrm{f}}$ is the feedback resistance
e．A C－B connected transistor has $\propto=0.96$ and $\mathrm{I}_{\mathrm{E}}=2 \mathrm{~mA}$ ．Find $\mathrm{I}_{\mathrm{c}}$ and $\mathrm{I}_{\mathrm{B}} \quad$（ 5 Marks）
f．Plot a graph of $I_{D}$ against $V_{G S}$ for a constant value of $V_{D S}$ and hence state the equation of $\mathrm{I}_{\mathrm{D}}$
g．What is forbidden gap？
（2 Marks）

## QUESTION TWO（20 MARKS）

a．Define the following terms：
i．Depletion layer
（2 Marks）
ii．Load line
（2 Marks）
b．Differentiate between ionic and covalent bonding
（6 Marks）
c．Why does a pure semiconductor behave like an insulator at absolute zero temperature
d．State and explain whether the zener－diode below in properly biased
（5 Marks）


## QUESTION THREE (20 MARKS)

a. State two types of field effect transitors.
b. Differentiate between:
i. Gate and drain terminals
ii. Drain and transfer characteristics
c. For a N -channel JFET, $\mathrm{I}_{\mathrm{DSS}}=8.7 \mathrm{~mA} \mathrm{~V}_{\mathrm{P}}=-3 \mathrm{~V}, \mathrm{~V}_{\mathrm{GS}}=-1 \mathrm{~V}$..Find the value of :
i. $\quad I_{D}$
(3 Marks)
ii. $\quad g_{\text {mo }}$
(3 Marks)
iii. $\quad g_{\mathrm{m}}$
(3 Marks)
d. Draw a basic symbol of a n-type JFET stating the gate, source and drain terminals
(3 Marks)

## QUESTIONS FOUR (20 MARKS)

a. Define the following terms:
i. Base transport factor
ii. Current gain
b. Prove that $\frac{\alpha}{1-\alpha}=\beta$ where $\beta$ is the current gain of a BJT (8 marks)
c. A C-E connected transistor has $\beta=100$ and $\mathrm{I}_{\mathrm{B}}=50 \mu \mathrm{~A}$ Find $\propto_{1,} \mathrm{I}_{\mathrm{c}}$ and $\mathrm{I}_{\mathrm{E}}$
a. What is load line of a diode?

## QUESTION FIVE (20 MARKS)

a. Define the following terms:
i. Slew rate
(2 marks)
ii. Output Impedance
(2 Marks)
b. Design a circuit showing how an OP-AMP can be used as:
i. Non Inverting Amplifier
(4 Marks)
ii. Summer
(4 Marks)
c. With the aid of a diagram differentiate between a forward and reverse biased diode
(8 Marks)

